

Smart Mobility

A. Aal
Volkswagen Commercial Vehicles, Wolfsburg,
Germany



Biography

Andreas (SM/CRP) drove the semiconductor strategy activities at VW for many years, focusing on technology capability enhancements for the most advanced node-based chip products, including improved integration schemes, as well as optimisation of power electronics module qualification (both in funding projects and direct collaboration with industry players). In 2023, he joined VW Commercial Vehicles as system architect and project manager for Mobility-as-a-Service computing system solutions. He has 26 years of experience in the semiconductor industry, has authored over 50 publications on reliability, has given tutorials at IEEE IRPS and IIRW, as well as invited/keynote speeches at various conferences/conventions. Andreas is chair of the German VDE ITG MN 5.6 WG on (f)WLR, reliability simulations and qualification, chair of the European chapter of the SEMI Automotive Advisory Council and serves the Federal Ministry of Education and Research (BMBF) industry advisory board on cybersecurity. He is also a founding member of the Fraunhofer Chiplet Centre of Excellence (CCoE) and active within JEDEC and DKE.

References

From Megawatts to Microseconds: Enabling Sustainable AI Through Power Semiconductor Innovation and Test

F. Marino
Managing Director
Advantest Italia S.r.l, Managing Director,
Vimercate, Italy

Abstract

Modern test systems are no longer simply production tools; they are strategic enablers of innovation. As AI reshapes the world's energy landscape, Advantest power test solutions help unlock next-generation power semiconductors full potential by ensuring uncompromising efficiency, reliability, and performance.

Biography

MARINO FABIO

Born in Italy in 1971. Hold a MS degree (1997) in electronics and a MBA (2012).

Joined semiconductor industry in 1998 as designer and application engineer for analog and mixed signal devices. Since 2007 moved to management level with responsibilities in leading application engineer teams and account management.

Joined Advantest in 2011 where currently holds the roles of MD for Advantest Italia S.r.l. and for CREA S.r.l.

References

Engineering for cyber resilience: Through-life modelling and analysis

S. Shaikh

Swansea University, Swansea, United Kingdom

Abstract

Coming Soon

Biography

Siraj Shaikh is a Professor in Systems Security at Swansea University (UK), where his research interests sit at the interface of automotive cybersecurity, systems engineering and cyber-physical systems security. Siraj is well published in automotive cybersecurity, and he currently is a Member of the College of Experts part of the UK's Department for Transport (DfT) Scientific Advisory Council (SAC).

Previously, he has been an Industrial Fellow to HORIBA MIRA, funded by the Royal Academy of Engineering (RAEng). More recently he served as an Independent Scientific Adviser to The Alan Turing Institute under the BridgeAI initiative. He serves as Chair for the Security Workstream of the Global Automotive Advisory Council at SEMI.

References