Future Disruptions

Chipstainability - A Megatrend to Strengthen Europe's Leading Role for Global Collaborations

S. Wunderer Government Funding Manager Nokia Solution and Networks GmbH & Co. KG, Ulm, Germany



Abstract

In times of geopolitical unrest, climate change as a global problem has to be tackled by closely collaboration with other nations. Europe should use its leading position in microelectronics sustainability to meet the needs of the present without compromising the ability of future generations. The example of ICT shows how strategic cooperation can be used to reduce energy consumption and embodied emissions in microelectronics. The future-oriented results of a recent IPCEI ME/CT Chipstainability Workshop are presented and discussed.

Biography

Stefan Wunderer is driving future topics in mobile communications since more than 40 years, mostly filling leading positions in network optimisation, customer support and training. Within Nokia, he is head of Nokia's IPCEI ME/CT chip design project in Ulm and Nürnberg and facilitating the RAN Energy Efficiency task force. Additionally, Stefan is lecturing sustainability at the University of Würzburg, leading an international SNS-JU research team for Social Needs and Value Creation as well as working within the Scientists for Future in Cologne. He is actively supporting the working group Women in Telecommunications and Research (WiTaR).

The Role of Time Critical Logistics in Complex Global Supply Chains

R. Schoenzetter Global Head of Business Unit High Tech & Semicon time:matters GmbH, Neu-Isenburg, Germany



Abstract

As semiconductor supply chains stretch across continents and rely on tightly sequenced production flows, even minor delays can lead to significant disruptions. In a world impacted by geopolitical shifts, supply shortages, and accelerating innovation cycles, the ability to respond quickly is no longer optional - it's a strategic necessity.

In this session, **Remy Schoenzetter**, Head of the Business Unit High Tech & Semicon at **time:matters**, explores the critical role of agile, time-sensitive logistics in securing continuity and responsiveness across the semiconductor value chain. Drawing on global experience in supporting semiconductor manufacturers and equipment suppliers, he will outline how time-critical networks, courier terminals, and 24/7 operational control are becoming increasingly important.

This talk offers a strategic perspective on how companies can rethink urgency, resilience, and agility - transforming logistics from a cost driver into a competitive edge.

Biography

Remy Schoenzetter is the Global Head of the High Tech & Semicon Business Unit at time:matters, a logistics service provider specializing in time-critical logistics.

With over a decade of experience in supply chain management, air freight, and freight forwarding, Remy brings a pragmatic, hands-on perspective to complex logistical challenges.

His career spans various roles across different logistical networks, including leadership positions in operations, customer service, and strategic partner management. Prior to his current role, Remy served as Head of Operations Western Europe, where he oversaw a team of logistics experts and led transformative initiatives across the region.

He is known for connecting cross-functional teams and translating customer urgency into solutions that drive performance and resilience.

Remy holds a Master's degree in Finance, complemented by certifications in Lean, Agile, and professional coaching. He combines analytical thinking with a people-centered leadership style, and currently contributes to the SEMI Supply Chain Initiative as a strategic partner.

Topic Coming Soon

T. Clarius Director EHS&S Globalfoundries, EHS&S, Dresden, Germany

Abstract

Coming Soon

BiographyComing Soon

Strengthening Semiconductor Supply Chains in an Era of Disruption

B. O'Dowd Head of Global Business Development Semicon Kuehne+Nagel, Business Development, Dublin, Ireland



Abstract

In this session, Barry O'Dowd, Head of Global Business Development Semicon, at Kuehne+Nagel, will explore the key risks facing semiconductor logistics and how companies can proactively mitigate them. He will introduce practical strategies for assessing and strengthening transportation resilience by drawing from real-world examples and lessons learned from industries with highly complex supply chains.

Attendees will gain insights into:

- How the semiconductor industry is adapting to global disruptions and reshoring trends
- The role of data-driven risk mitigation tools in evaluating transportation lanes
- Best practices for securing critical shipments, from wafer fabrication materials to capital equipment
- The importance of continuous risk assessment in an evolving supply chain landscape

With decades of experience optimising Semicon logistics, Barry will share how industry leaders can turn supply chain resilience into a competitive advantage, ensuring stability, security, and seamless operations in a rapidly changing world.

Biography

Based in Dublin, Ireland, Barry brings more than 30 years of international logistics expertise, In his current role, Barry leads strategic growth and innovation within the company's semiconductor logistics segment—one of the key focus areas under Kuehne+Nagel's global Roadmap 2026 strategy. Recognizing the industry's unique supply chain demands, he has been instrumental in developing SemiconChain—a dedicated, quality-certified network now spanning more than 35 locations across major semiconductor hubs.