

New Fab Ramp-up Vertical Excellence

From Complexity to Advantage: Strategic Levers for Future Ready Fabs

G. Notarnicola
Partner
Porsche Consulting, Milan, Italy

Abstract

In a climate of geopolitical instability and market contraction, especially in automotive and energy-linked segments, European semiconductor players must rethink how to stay competitive. Can AI truly revolutionize one of the most time-consuming and costly steps in the fab journey, such as product qualification? Possibly, but only if companies learn to unlock the value of the data they already have.

This session explores how collaboration across traditionally siloed domains, technology, operations, and digital, can generate real-world efficiency and speed. We'll share concrete examples and pragmatic insights from the field, showing how to build the right ecosystem of specialized capabilities to enable faster, more resilient fab scale-up.

Biography

TBD

References

Optimizing Fab Performance: Proven Strategies from Materials to Manufacturing

A. Amade
President Europe and the Middle East (EMEA)
Region
Entegris, Moirans, France



Abstract

In a world of rising complexity, speed, and billion-euro investments, fab startups face unprecedented pressure. Our presentation outlines key strategies for effective contamination and material management, and shares concrete, experience-based insights to support fab ramp-up and operational efficiency. With a focus on risk mitigation, digitalization and data sharing, environmental responsibility and workforce training, we provide proven and innovative solutions from specification definition to startup and beyond.

Biography

As president of the Europe and the Middle East (EMEA) region at Entegris, Antoine Amade is responsible for driving regional strategies and leading efforts to expand into markets that can benefit from Entegris' unparalleled expertise in advanced purity and materials solutions. These include sectors such as semiconductor, desalination, clean hydrogen energy, and other high-tech and data-intensive industries.

With 30 years of experience at Entegris, Mr. Amade has held leadership roles in gas and liquid microcontamination market management, strategic account management, and regional sales management. He has also held business management positions overseeing the market in North America.

He holds a degree in Chemical Engineering from ENS Chimie Lille and is an active member of the SEMI Electronic Materials Group and the Global Automotive Advisory Council for Europe.

References

M. Puttock
Sr. Director - Technology and Innovation
Entegris, Dresden, Germany



Biography

Mark Puttock PhD has worked in the semiconductor industry for over 30 years with a background in Physics and Plasma processing. From 2014, Mark follows technology trends and collaborates with Entegris' global product development teams to develop timely and differentiated new materials and components for the world's leading semiconductor manufacturers.

References

Panelist

J.-R. Lèquepeys
Deputy Director and Chief Technology Officer
CEA-Leti, Grenoble, France



Abstract

Panelist of the session sponsored by Entegris:

End-to-End Manufacturing Excellence: Designing, Starting Up, and Scaling Europe's Fabs for Competitive Advantage

Thursday, November 20 | 13:40 - 15:00

Executive Forum | Hall C2

Biography

Jean-René Lèquepeys received his engineering degree in 1983 from CentraleSupélec and taught physics for 2 years in Ouarzazate, Morocco. He joined CEA in October 1985, in Saclay, within the Central Security Office, in the laboratory for the evaluation of means of detection and intrusion. In 1987, he became head of the laboratory. In 1993, he was recruited by DSYS, at LETI in Grenoble, as an R&D engineer in the field of image processing. He then led projects in the field of "Telecom" at LETI before becoming the head of "Telecom, Communicating Objects and Smart Card" programs in 1999, within the team of Jean-Frédéric Clerc.

In 2005, he took charge of the Circuits Design Department at LETI / DSYS, and then created, in partnership with the Ecole des Mines de Saint-Etienne, a laboratory dedicated to electronic components safety analysis located in Gardanne (Paca). In 2010, from the joint initiative of both LETI and LIST Directors, he launched the DACLE division, relying on an original bi-site and bi-institute model, and focusing on Electronic Architectures, Integrated Circuit Design, and Embedded Software.

He participated in the creation of the Division DCOS (Division of Silicon Components) in 2011, and then led the division until the end of 2017, when he returned to head the DACLE division in 2018. In 2000, Jean-René Lèquepeys received the prestigious award from SEE, the "Grand Prix de l'électronique Général Ferrié," for his work in the telecommunications field. He is the author of about fifteen patents in this field.

In 2019, he was appointed Deputy Director in charge of programs at LETI and Deputy Director to the Director of LETI.

References

Precision at Scale: How Europe Builds Its Wafer Fabs

H. Blaschitz
Executive VP of Advanced Technology Facilities
Exyte, Singapore, Singapore



Abstract

Europe is entering a new era of semiconductor manufacturing - one defined by precision at scale. This presentation explores how European engineering and construction teams are demonstrating the capability to deliver world-class wafer fabs that meet the highest standards of sustainability, safety, and technical precision.

Significant progress has been made in execution speed, quality, and digitalization, proving that Europe can build competitively on the global stage.

At the same time, higher construction and labor costs continue to challenge competitiveness and demand new levels of efficiency, modularization, and collaboration across the supply chain.

Europe's journey is still unfolding - yet its focus on innovation, reliability, and long-term value is shaping a distinctly European approach to building the world's most advanced factories.

Biography

Herbert Blaschitz, Executive VP of Advanced Technology Facilities at Exyte, is a recognized leader in the semiconductor industry. He has played a pivotal role in the profitable expansion of Exyte's semiconductor business, growing it from 1 billion euros in 2014 to over 6 billion euros in 2023.

Before joining Exyte, he held various positions at Jenoptik, Asyst, and Siemens Semiconductor (now Infineon). Originally from Austria, Herbert has lived and worked in Germany, France, the USA, and currently resides in Singapore.

Herbert Blaschitz earned a degree in electrical engineering from HTBL of Klagenfurt, Austria, and a degree in business administration from GSBA in Zurich, Switzerland.

References

Panelist

O. Aubel
Corporate Lead Automotive Solutions, Principle
Staff Program
GlobalFoundries, Dresden, Germany

Abstract

Panelist of the session sponsored by Entegris:

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Biography

Dr. Oliver Aubel has been leading the corporate implementation of GlobalFoundries Automotive Solutions for several years. In this role, he drives both technical and organizational transformation to meet the stringent requirements of automotive semiconductor manufacturing across all GlobalFoundries manufacturing sites. His work focuses on aligning advanced fab operations, automotive technology qualifications, and quality management systems with evolving industry standards — ensuring robust quality, reliability, and compliance throughout the global supply chain.

Prior to his current position, Dr. Aubel led the reliability team at GlobalFoundries (Fab 1) and managed multiple global quality initiatives. He holds a diploma (M.S.) in Electrical Engineering (2000) and earned his PhD in 2004, specializing in microelectronics reliability. He began his career 2004 in the reliability department at GlobalFoundries (formerly AMD) in Dresden, Germany, where he continues to work.

Dr. Aubel has authored or co-authored over 80 publications and holds several patents in the field of reliability. He is an active contributor to various industry consortia focused on reliability and automotive standards.

References