

Future Disruptions

Disruptive and Sustainable Bonding Technology Covering Various Material Combinations for Emerging Applications



S. Lutter Vice President R&D SUSS MicroTec SE, Vice President Research & Development, Sternenfels, Germany



Abstract

This presentation introduces a disruptive bonding technology which was derived from traditional anodic bonding but offers significant advances in terms of material versatility, surface preparation and sustainability. These advances include a significant reduction in the actual bonding process time, not only for traditional material combinations but more importantly for bonding a variety of materials which are used in emerging applications like 5G / 6G and power devices that help to improve the efficiency of electric vehicles. This results in significant direct and indirect energy savings on the one hand but on the other hand also offers significant total cost of ownership benefits for the actual bonding process itself.

Some latest process results for traditional MEMS use cases but also for emerging applications in the RF MEMS and power devices application spaces will be explained in order to illustrate the disruptive nature of the new process.

The new bonding technology was developed with a strategic partner and is now exclusively available on SUSS MicroTec's bonding equipment.

Biography

- Diploma in Microsystems Engineering from University of Applied Siences in Regensburg, Germany and Master of Business and Engineering from Steinbeis University Berlin, Germany

- Held various positions in process- development and R&D for MEMS, working in Switzerland and Germany, before joining SUSS MicroTec in 2008

- Held positions in Product Management for Coater- and Bonder Business Units
- Business Unit Manager Bonder since 2013
- Business Unit Manager Coater from 2017-2019
- Managing Director of SUSS MicroTec Lithography GmbH and SUSS MicroTec Photomask Equipment GmbH & Co. KG from 2018 2022
- Managing Director of SUSS MicroTec Solutions GmbH & Co. KG since 2022

Advanced Packaging Disruptions



M. Dropiński TA to DCAI & NEX Poland General Manager at Intel Invest in Pomerania, Gdańsk, Poland



Abstract

Advanced Packaging enables new era of chip design. It opens door for innovations way beyond traditional transistor miniaturization. What we can expect in this area and how it will affect power efficiency and scalability of future semiconductors? The presentation of Mieszko Dropiński from the Polish branch of Intel - a partner of Invest in Pomerania - will attempt to answer these questions.

Biography

Mieszko Dropiński is a business & technology leader, MBA advocating for building silicon & digital valley in Central Europe. Strategist and tech voice involved throughout ecosystem projects in building Europe's semiconductor industry resilient. Experienced in Business & Technology Management. Currently associated with Intel's largest R&D Center in Europe. Trusted advisor uniquely coupling technology with the business.

Engineering and transformation manager with a successful track record in leading international, highperforming teams - both engineering & sales. On a daily basis he serves as Change Manager navigating through a complex, ambiguous environment. Mentor for professionals from Top500 companies, Forbes 30under30 laureates. D&I ambassador.

Always staying positive and enjoying life. In his spare time, he catches wind & waves – kitesurfing is the biggest passion in his life, just after making the world a better place.

Feel free to add the speaker to your network @ LinkedIn: linkedin.com/in/mieszko-dropinski/

Zero defects matter | The Power of Xray in Advanced Packaging



C. Driller Vice President R&D Comet Yxlon International GmbH, R/D, Hamburg, Germany



Abstract

The semiconductor industry faces numerous challenges in the development and manufacturing of advanced packages. From a technical standpoint, these challenges include miniaturization, thermal management, and interconnect technologies. From a market perspective, challenges arise from higher production mixes due to application-specific integrated circuits (ASICs) and customers' intolerance for failures, particularly in critical automotive applications. These challenges have resulted in constantly increasing costs for designing and manufacturing ICs.

Consequently, the industry is adopting two key approaches. Firstly, it is embracing lights-out manufacturing, which involves fully automated factory operations that offer increased productivity, improved repeatability, and consequently, enhanced quality. Secondly, new testing strategies are being implemented to provide data for advanced process analytics, enabling a shift from reactive to predictive actions. These strategies aim to improve traceability, yield, and overall operational efficiency.

In the monitoring of interconnect characteristics such as diameter, height, co-planarity, and bump quality, inspection tools play a crucial role. Advanced X-ray technology, in particular, holds significant potential in driving the development of defect-free advanced packaging solutions through identifying root causes of failures.

Biography

Since 2020, Christian Driller has held the position of Vice President of Research and Development at Comet Yxlon, where his team spearheaded the development of cutting-edge x-ray and CT inspection solutions. Under his leadership, he has successfully established an agile R&D organization with a strong customer-centric focus, fostering a passionate and results-oriented team.

Prior to his current role, Christian Driller assumed the position of Vice President of Business Excellence at Comet Yxlon in 2017. In this capacity, he played a pivotal role in driving the professionalization efforts across all functional areas of the company.

Christian's professional journey commenced in 2012 within the automotive industry, where he served as a Business Consultant at Porsche Consulting. His primary focus was on optimizing and restructuring R&D departments within both automotive manufacturers and suppliers, delivering impactful results.

Christian Driller holds a Master's degree in Finance from ESB Reutlingen University and a Bachelor of Engineering from Baden-Wuerttemberg Cooperative State University. Notably, during his undergraduate studies, he collaborated closely with Dr. Ing. h.c.F. Porsche AG, serving as his cooperating company.

Chiplets - Accelerating System Innovation in the Era Heterogeneous Integration



L. De Ambroggi Segment Director Solutions Marketing Intel Foundry Services, Munich, Germany



Abstract

The semiconductor industry is undergoing a structural transformation driven by technology innovation, as well as supply-chain disruption, like the move from SoCs to "Chiplets" and the vertical integration of OEMs and CSPs in various industry domains.

These trends are also coupled with increased R&D costs for advanced node technologies, required to cope with rising performance requirements in several applications.

All the above is expected to propel a significant growth in the semiconductor market for leading-edge nodes in data processing industry segments, like Mobile, Compute, Telco and Automotive.

Learn about how IFS is uniquely positioned to address this transformation by creating an "open system foundry" that enables our customers to differentiate and lead in their markets by creating full-stack solutions from their choice of the best of Intel and the foundry industry ecosystem, delivered from a secure and sustainable source of supply.

Biography

Luca De Ambroggi, Director, Marketing and Platform Solutions, at Intel Corporation.

Luca is a veteran in the semiconductor and automotive industry with more than two decades of experience in various domains spanning along the entire supply chain.

He joined the company with 25+ years of semiconductor industry experience, including product and design management and technical marketing.

He began his career as a design engineer with STMicroelectronics and has held various technical and marketing positions within both STMicroelectronics and Infineon.

Before joining Intel, Luca contributed to develop the automotive electronics and semiconductor research at IHS Markit, providing advisory services on advanced Infotainment and ADAS/AV systems, as well as AI technologies.

Luca has more than 20 patents issued while working in the semiconductor industry.

He graduated full marks from the University of Catania in Electro-Technic Engineering. He is fluent in Italian, English, and German.

How Cryogenic Cooling can Enable the Future of Computing - or Block It



A. Regnat Managing Director kiutra, Munich, Germany



Abstract

Quantum technologies promise to solve some of the most pressing problems of our time, e.g., by means of quantum computers or quantum-enhanced sensors. Because of the sensitivity of most quantum mechanical systems with respect to thermal excitations, ultra-low temperature (cryogenic) cooling is essential for both the development and the operation of scalable quantum systems. In this talk, I will discuss how new cooling paradigms based on solid-state magnetic cooling support the adoption of quantum technologies by shortening innovation cycles, improving scalability, as well as building resilience by avoiding the use of critical resources.

Biography

Alexander Regnat, Managing Director at kiutra, holds a PhD in physics from the Technical University of Munich, Germany, for his low-temperature research on novel intermetallic compounds. With a strong background in cryogenics, he co-founded kiutra in 2018 to support the advent and industrial adoption of quantum technologies by means of sustainable magnetic cooling.