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Chips Hub Europe

Building Collaborative ECS projects via the Eureka Cluster Xecs



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Abstract

The semiconductor industry and actors from the full Electronic Components and Systems value chain innovate, and evolve, by a simple creed: faster, greener, cheaper. And while it might seem complex and costly to introduce novel technologies at every step of production, it is easier and more affordable than you think to implement change and gain an immediate return on investment. Whether you need to manage fierce competition or cut innovation costs, funded international collaborations are your gateway to tackle complex technological challenges, address research opportunities, grow innovative partnerships to drive technological improvement and increase profitability.

Join our funding programme representative and experts and discover how funding and international industry collaborations including other actors can help your company to adopt next-generation innovations ahead of the global competition!

Biography

With an academic background in biology and immunology, Nadja Rohrbach spent several years as a scientific officer for a member of the German Federal Parliament. Subsequently she joined the DLR Project Management Agency, initially focusing on Eureka and network projects, before becoming head of the Eurostars group. Few years ago, Nadja was seconded by the German Federal Ministry of Education and Research to the Eureka secretariat in Brussels, where she was appointed central coordinator for the new Eureka Clusters Programme and worked with various bodies like public authorities and industry to draft the first Eureka Clusters Multi-Annual and Annual Operational Plans. One year ago, she started at AENEAS as Xecs Programme Director being in charge of the Clusters Penta and Euripides as well.

Smart Test Cells: Improving Efficiency and Convenience



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Abstract

While testing is the quality gate assuring that only good products go to the customer it does not improve the product and thus must be as efficient as possible. Test setups (Test Cell) comprise more and more equipment types (wafer probers, final test handlers, laser markers, vision inspection systems, automatic reel changers, AMHS, testers, ...). Typically testers have the longest lifetime in the test cell, especially if they are not the bottleneck equipment that slow the overall setup down. ITEC will showcase how a test cell can be automated and integrated when the tester becomes the master equipment including wafer map handling and post processing like DPAT and Maverick Wafer Handling in order to increase the output.

Biography

Before migrating to the Netherlands, Felix Patschkowski graduated from the Technical University of Hamburg with a master's degree in computer science and engineering and started as an automation engineer at Nexperia's wafer fab in Hamburg. Being responsible for the automation of the wafer test department, he was exposed to ITEC's technology, especially the tester platforms, right from the beginning. On a business trip to Nijmegen, he fell for the city and technology. Soon after, he started working for ITEC to develop software for existing and new testers. Over time, he grew into the position of a software architect for the latest test platform under development – the MegaParset. Next to his career he is also active in Olympic saber fencing.