

SEMICON® EUROPA

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ATREG PANEL

Overview Of The Global Semiconductor Manufacturing Asset Market



S. Rothrock
Founder, President & CEO
ATREG, Inc., Washington, United States of
America



Abstract

Coming Soon

Biography

Stephen founded ATREG [www.atreg.com] in 2000 to help global advanced technology companies divest and acquire infrastructure-rich manufacturing assets, including wafer fabs (front- and back-end) as well as MEMS, solar, display, and R&D facilities. Over the last 20 years, his firm has completed 40% of all global operational wafer fab sales in the semiconductor industry, a total of 100 transactions representing a value of over \$5 billion. Recent global acquisitions and dispositions have involved Allegro MicroSystems, Fujitsu, GF, IBM, Infineon, Matsushita (Panasonic), Maxim, Micron, NXP, onsemi, Sony, Qualcomm, Renesas, Texas Instruments, and VIS to name just a few.

Prior to founding ATREG, Stephen established Colliers International's Global Corporate Services initiative and headed the company's U.S. division based in Seattle, Wash. Before that, he worked as director for Savills International commercial real estate brokerage in London, UK, also serving on the UK-listed property company's international board. He also spent four years near Paris, France working for an international NGO. Stephen holds an MA degree in Political Theology from the University of Hull, UK and a BA degree in Business Commerce from the University of Washington in Seattle, USA.



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Panelist



P. Leinenbach
Senior Vice President, Automotive Electronics
Robert Bosch GmbH, Dresden, Germany



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Biography

During his 24-year career at the company, Dr. Leinenbach has held positions all over the world in France, Germany, and China where he served as General Manager at Robert Bosch Automotive Products as well as General Manager at Robert Bosch Electronics Trading in Suzhou in 2015. In 2017, he became Senior Vice President and Technical Plant Manager at the company's Reutlingen, Germany plant as well as part of the Board of Management of Robert Bosch Semiconductor Manufacturing at the Dresden, Germany plant. In 2022, he became Senior Vice President in charge of all manufacturing semiconductor supply chain operations. Dr. Leinenbach holds a PhD in Physics from the national German research institution Forschungszentrum in Jülich, Germany.

Panelist



A. Gorski
Executive Vice President, Back-End Operations
Infineon Technologies AG, Munich, Germany



Abstract

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Biography

Alexander is responsible for Infineon Technologies AG's global back-end operations cluster which combines all in-house and external manufacturing. The in-house manufacturing footprint consists of 11 sites located all around the world. In his role as Cluster Head, he is also in charge of package innovation, test technology development as well as manufacturing automation and digitalization. Previously, Alexander was a Board Member of Infineon Technologies AG's Power & Sensor Systems Division. Serving different global markets in power management and sensor systems, such as computing, consumer, industrial, and automotive, the division holds a leading position in a variety of applications. In his role as Division Chief Operating Officer, Alexander has overseen global operations as well as technology, innovation, and software. Before re-joining Infineon in November 2016, Alexander served for almost a decade as a Member of the Management Board and Group COO at Conergy, a leading global renewable energy company focused on solar. During his tenure, he was instrumental in helping the company transition from a solar component manufacturer into a fabless and asset-light project development and turnkey EPC company.

Alexander started his professional career in the semiconductor industry at Siemens Semiconductor, later carved out as Infineon Technologies. During his time with Siemens and later Infineon, he served in several management positions in operations, supply chain, and key account management.

Alexander holds an MBA from the University of Regensburg, Germany.

Panelist

G. Meyer
Chief Operating Officer
Elmos Semiconductor SE, Dresden, Germany



Abstract

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Biography

Guido started his career as a hardware engineer for an electronics company. In 1995, he joined Elmos where he developed test machines for semiconductors. He subsequently led the company's test area for seven years. From 2012 to 2016, Guido served as division manager responsible for wafer production. In 2017, he became a member of the company's management board and took over responsibility for the production division. Guido holds an MSc degree in electrical engineering from the University of Applied Sciences in Dortmund, Germany.