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Advancements in Wireless Tech

Engineered Substrates and Materials for 5G



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Abstract

information coming soon

Biography

Cesar Roda Neve was born in Madrid, Spain, in 1975. He received the Msc. Engineer degree from the ICAI Universidad Pontificia de Comillas, Madrid, Spain, in 2000. In 2012, he received the Ph.D. degree in engineering sciences from the Université catholique de Louvain (UCL), Belgium. From 2004 to 2006, he was with the Electronics Department of the University Carlos III of Madrid, Spain, where he worked on ROF links and optoelectronic devices. From 2006 to 2012, he joined the Microwave Laboratory at the Université catholique de Louvain (UCL), Belgium, where he worked on the characterization and application of Si-based substrates for RF integration, in particular the use of HR-Si, HRSOI, and trap-rich HR-SOI substrates, non-linearities and parasitic effects. From 2013 to 2016, he was with the 3D and Optical Technology group at IMEC, where he worked on signal integrity, power delivery networks and RF modeling with special attention to 3D stacking and packaging. From 2016 to 2020, he worked at M3Systems Belgium as project manager for GPS, interferences and satellite related projects. In 2021 he joined SOITEC Belgium as R&D Program Manager. His research interest are new applications for SOI substrates for RF, with focus in 5G and 6G communications.